



SRPEC

RESUME

Document No. HR021

Revision No. 00

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(Jitendra Patel)

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Objective

To work in a progressive organization that incorporates varied spectrums of work & diversity, this endows consistency, competency & expertise in professional as well as social spheres, enabling collective excellence and technical growth, personal fulfillment with welcome attitude for new ideas and concepts to enhance the overall growth of the organization.

Academic Profile

Educational Qualification

Qualification	Stream	Institution	University/Board	%/CGPA
M.TECH	VLSI	T.I.T college	RGPV. Uni.	7.87 CGPA
BE	EC	C.U.Shah college of Engg & Tech	Saurashtra Uni.	69.5%
HSC	Science	S.P.vidhyalay	GHSEB	66%
SSC		R.A.Patel Boys Highschool	GSEB	73.7 %

Software Skills

- KEIL software for 8051 , 8086 simulator
- Cisco Packet Tracer
- MATLAB
- Multisim
- Proteus.vsm

Project

Title : Implementation and Performance Analysis of Low power Adder/ Subtractor Using 45NM CMOS Technology.

Objective : To Reduce Power Consumption and Energy dissipation

Application : Can be used to make a battery of different Portable Devices

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Professional Highlights

Experience

Name of the Organization: K.P.T.I.T College of diploma Studies, Sokali
 Designation: Assistant Professor
 Duration: 6 months
 Nature of Duties Performed: Lectureship

Name of the Organization: Smt. S.R. Patel Engineering College, Unjha
 Designation: Assistant Professor
 Duration: 4.5 Years
 Nature of Duties Performed: Lectureship

State and National Level Conferences

- NCIET , 2013 at Smt S.R.Patel Engg college , Dabhi - Unjha
- NCIET , 2014 at Smt S.R.Patel Engg college , Dabhi - Unjha
- NCIET , 2015 at Smt S.R.Patel Engg college , Dabhi - Unjha

Seminars and Workshops

- 12 days Microcontroller based Application workshop by Peahend tech
- Robotics workshop by Technophiliya at SRPEC
- Digital signal Processing STTP at
- FDP by Dhyey Training academy and Placement consult
- Workshop on Embedded system and Its Application

Strengths

- Software like LTspice , Pspice , KEIL ,Flashmestic
- Teaching Skill

Interest Areas

- Microprocessor And Microcontroller
- VLSI System And Design
- Embedded System

Extra-Curricular Activities

Awards and Achievements

- I got GTU Result Subject award for microprocessor and Interfacing in 2013
- I got GTU Result Subject award for Embedded system , Advance processor and microprocessor & interfacing in 2014 .

Participations

- Was member of Organisation committee in NCIET ,2013 held by SRPEC college ,DABHI -Unjha
- Was member of Co-coordinator of Prakrusht -2013 held by SRPEC college
- Have given many presentations during college
- Was a Co-ordinator of Prakrusht -2014 held by SRPEC college.
- Was a EC branch Track Co-ordinator of NCIET 2015 held by SRPEC college

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Personal Sketch

Date of Birth : 5th Sept ,1987
Blood Group : B+
Permanent Address : 63 House , Navarang Soc-3 , 80 Feet Road ,Wadhwan city-363030
Gender : Male
Marital Status : Married
Languages known : English, Hindi, Gujrati
Nationality : Indian
Hobbies : Teaching, Reading, Travelling

I hereby declare that the above information is true to my belief and my knowledge.

Date:
Place: Unjha

(Name): Jitendra Patel
(Signature)



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